

Notice of References Cited

Application/Control No.

10/059,558

Applicant(s)/Patent Under
Reexamination
HOFMANN ET AL.

Examiner

Gopal C. Ray

Art Unit

2111

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,070,205	05-2000	Kato et al.	710/100
	B	US-5,796,413	08-1998	Shipp et al.	345/522
	C	US-5,923,859	07-1999	Melo et al.	710/113
	D	US-5,619,726	04-1997	Seconi et al.	710/22
	E	US-5,970,234	10-1999	Jin, Sung-Kon	710/111
	F	US-			
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	I	US-			
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	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"VLSI design of a bus arbitration module for the 68000 series of microprocessors" by Ososanya, E.T.; McGlone, M.D.: Strong, T.D. (abstract only)
	V	"An efficient controller scheme for MPEG-2 video decoder" by Nam Ling; Nien-Tsu Wang; Duan-Juat Ho (abstract only)
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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